

FIG. 1

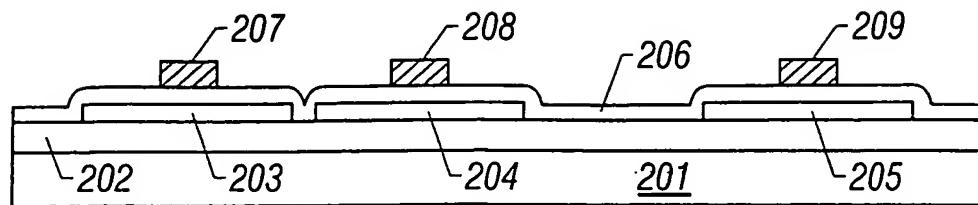


FIG. 2A

DRIVER CIRCUIT TFT → PIXEL TFT

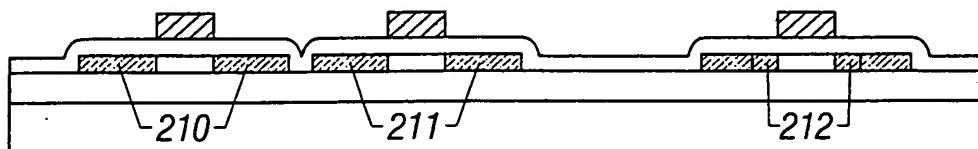


FIG. 2B

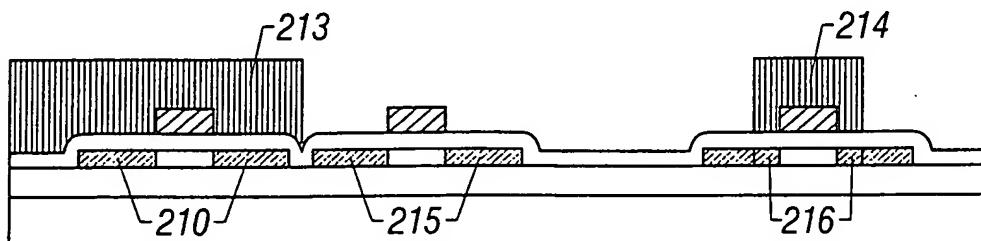


FIG. 2C

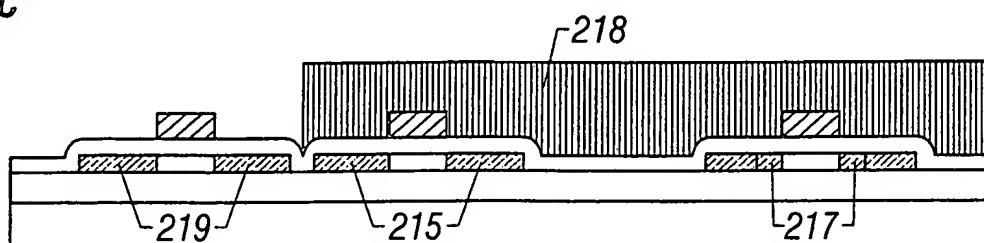


FIG. 2D

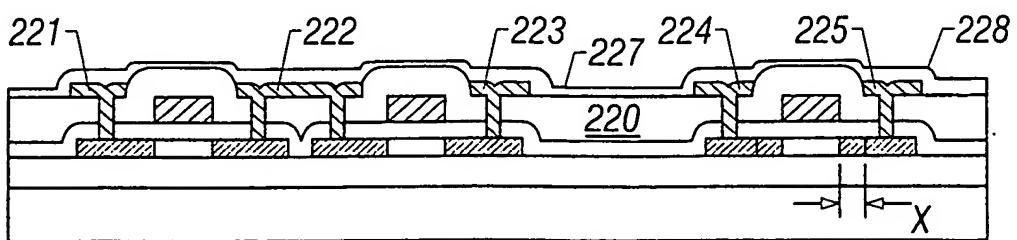


FIG. 2E

DRIVER CIRCUIT TFT → PIXEL TFT

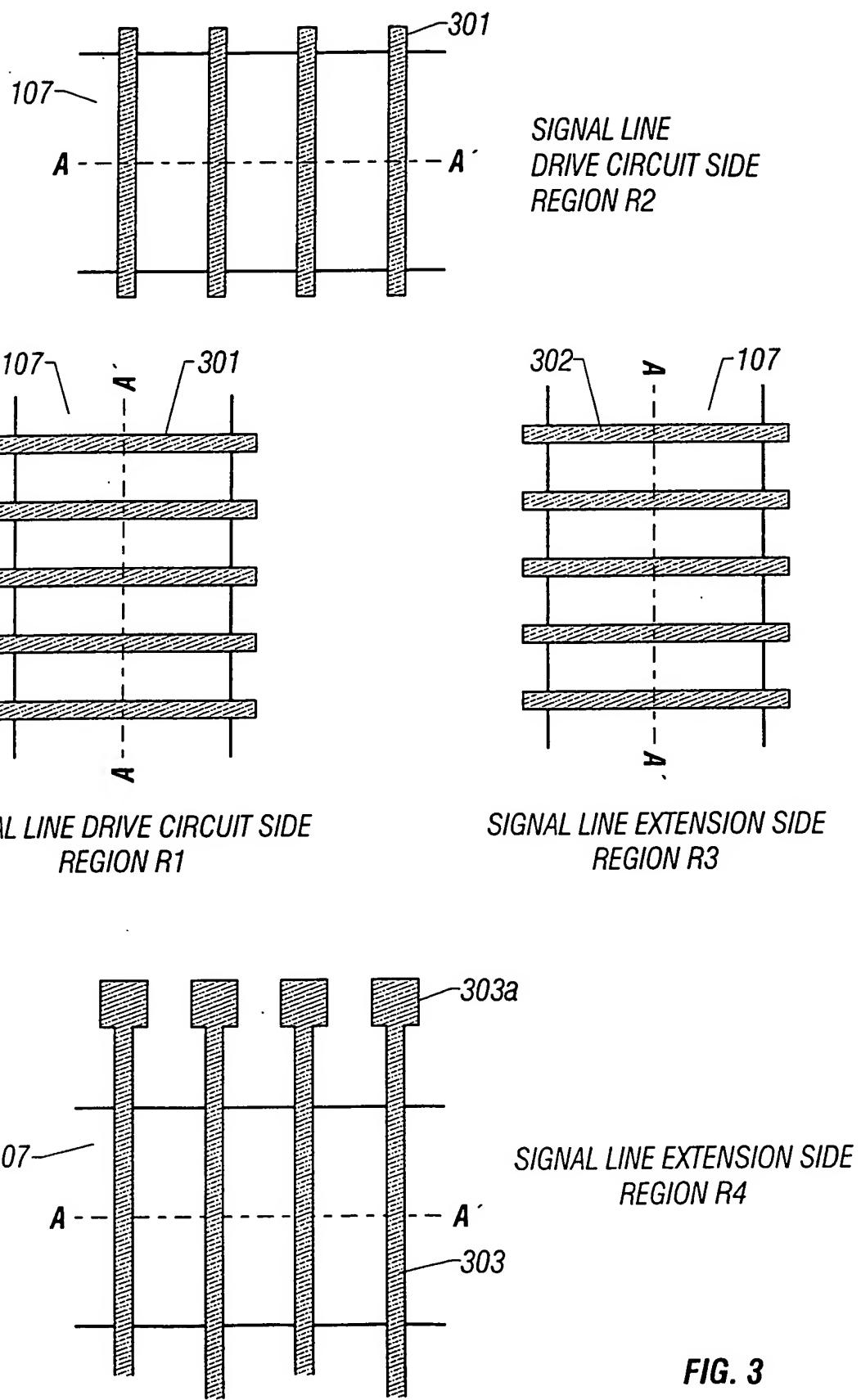


FIG. 3

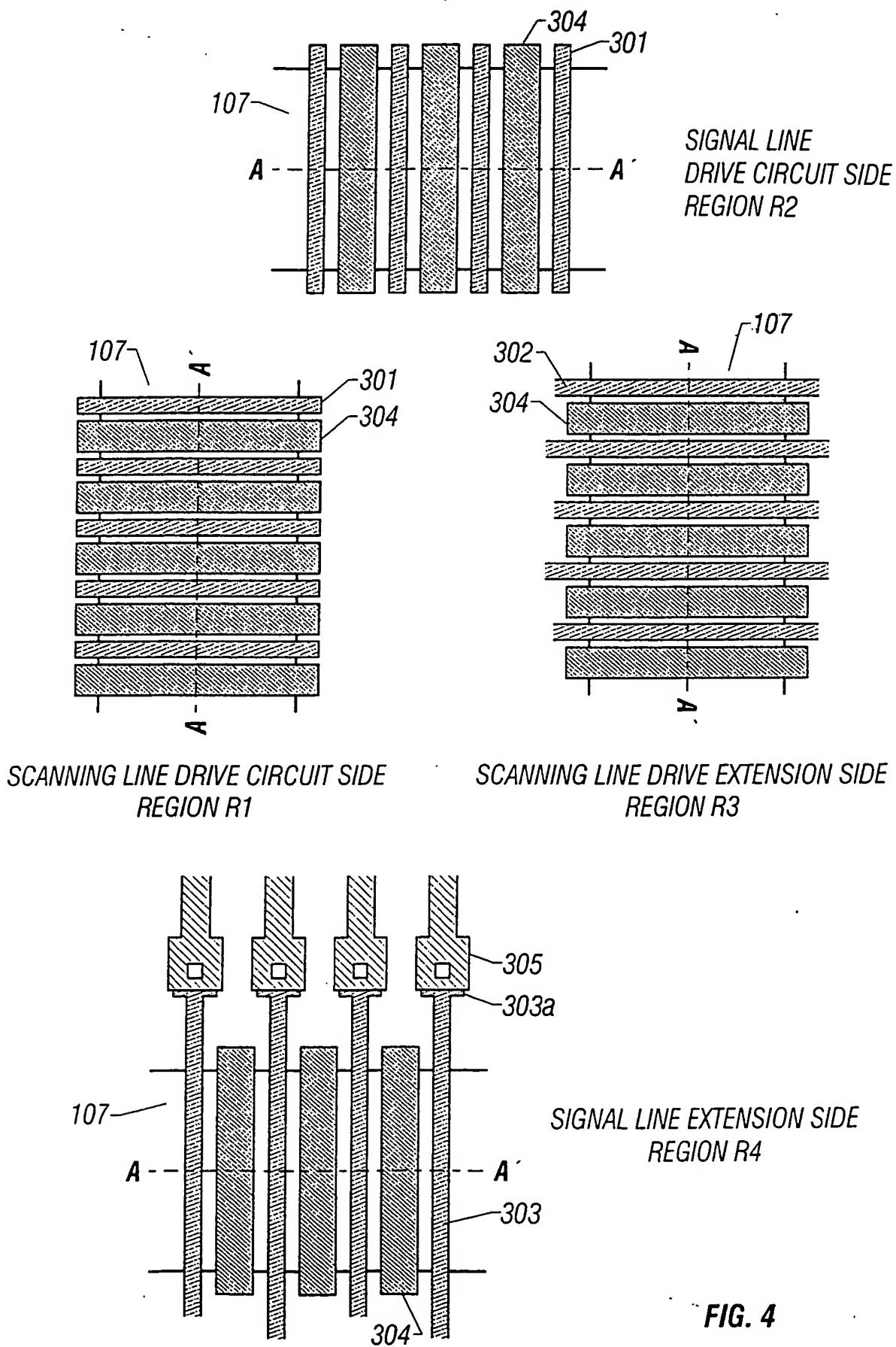


FIG. 4

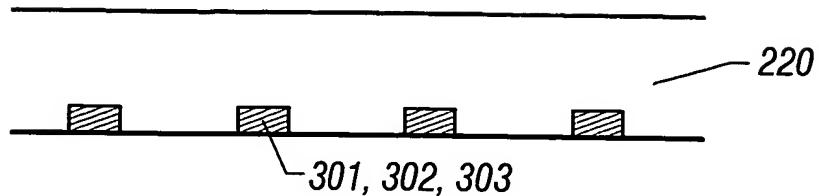


FIG. 5

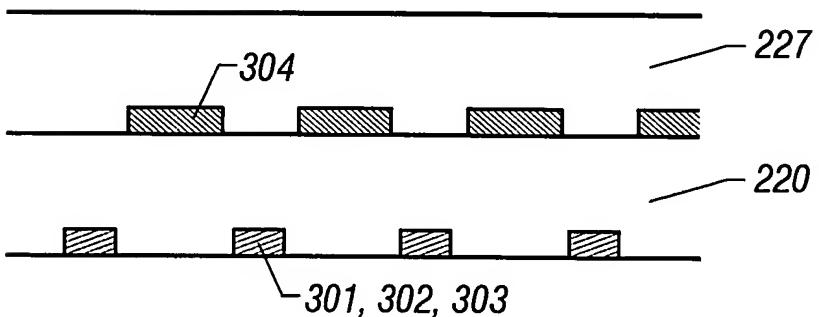
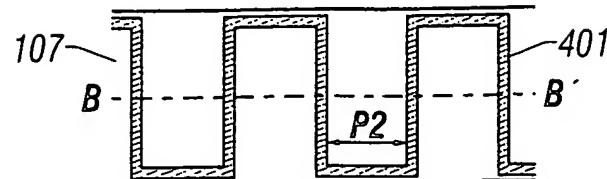
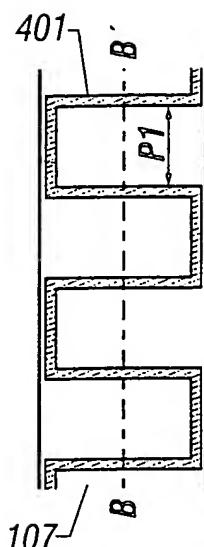


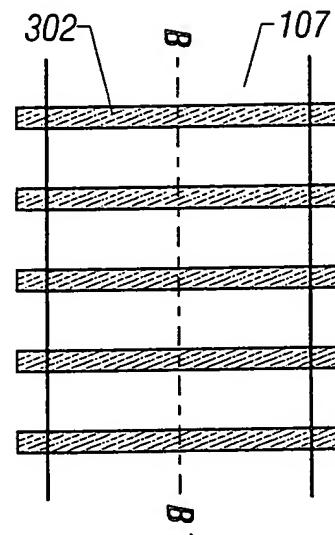
FIG. 6



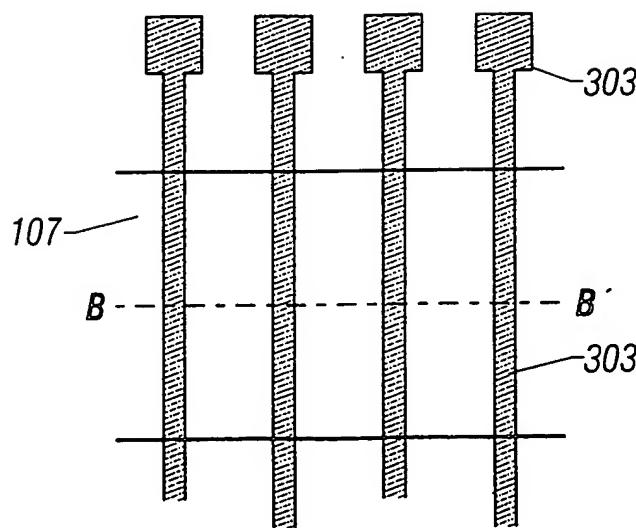
SIGNAL LINE DRIVE CIRCUIT SIDE
REGION R2



SIGNAL LINE DRIVE CIRCUIT SIDE
REGION R1

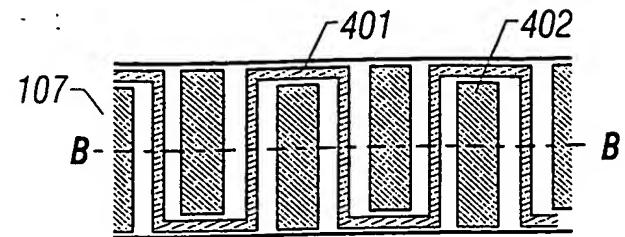


SIGNAL LINE EXTENSION SIDE
REGION R3

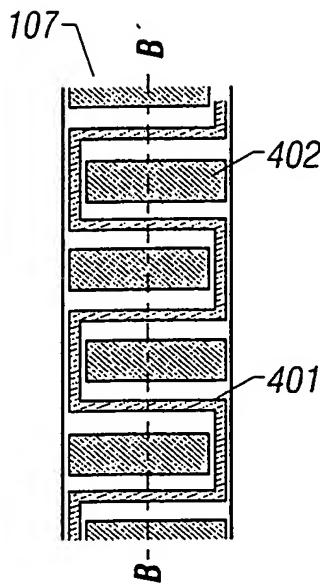


SIGNAL LINE EXTENSION SIDE
REGION R4

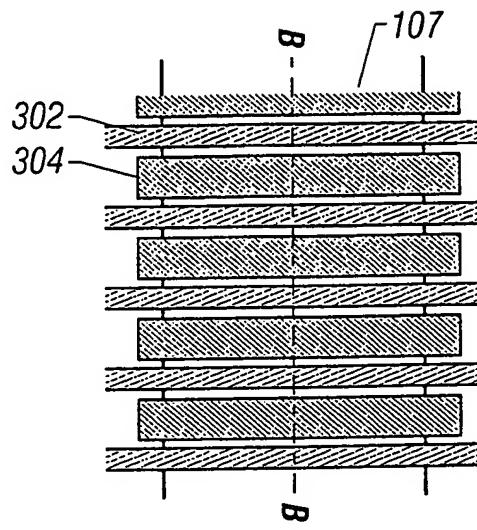
FIG. 7



SIGNAL LINE DRIVE CIRCUIT SIDE
REGION R2



SIGNAL LINE DRIVE CIRCUIT SIDE
REGION R1



SIGNAL LINE EXTENSION SIDE
REGION R3

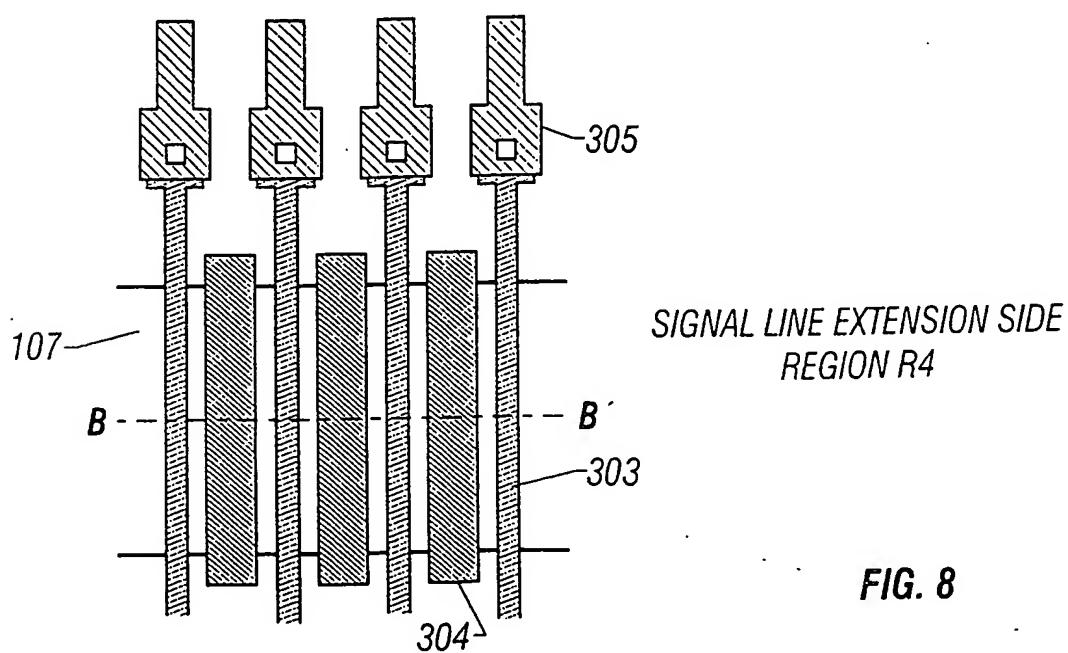


FIG. 8

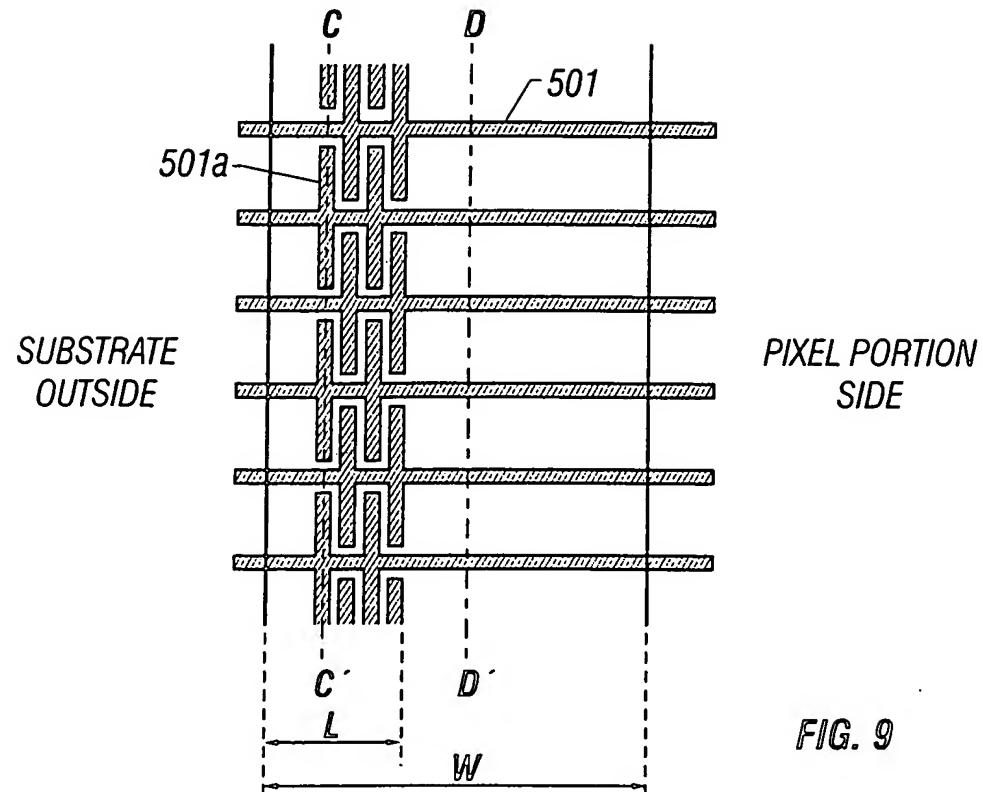


FIG. 9

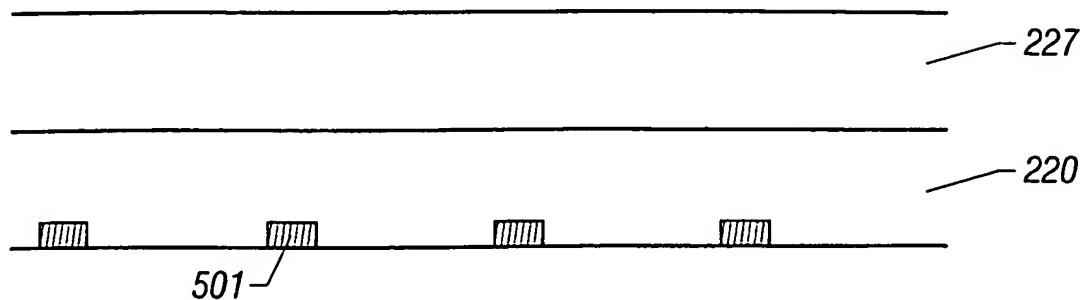


FIG. 10

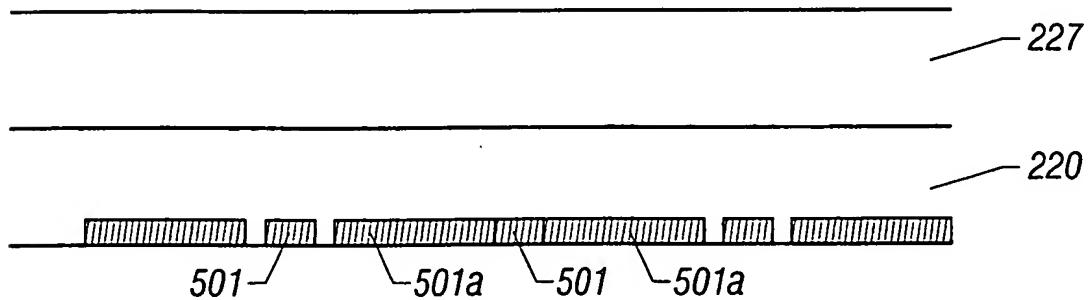


FIG. 11

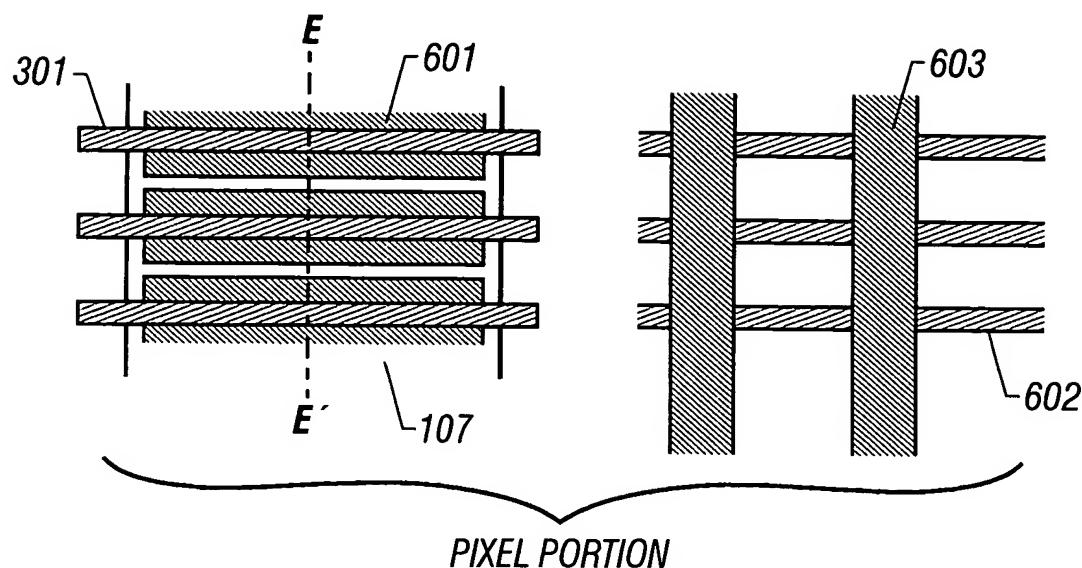


FIG. 12

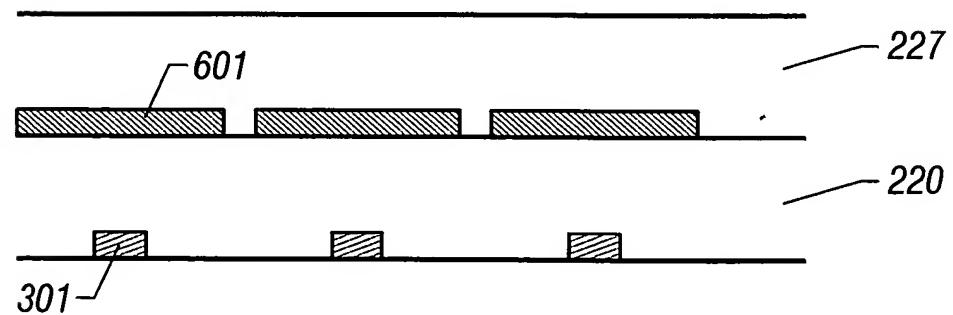


FIG. 13

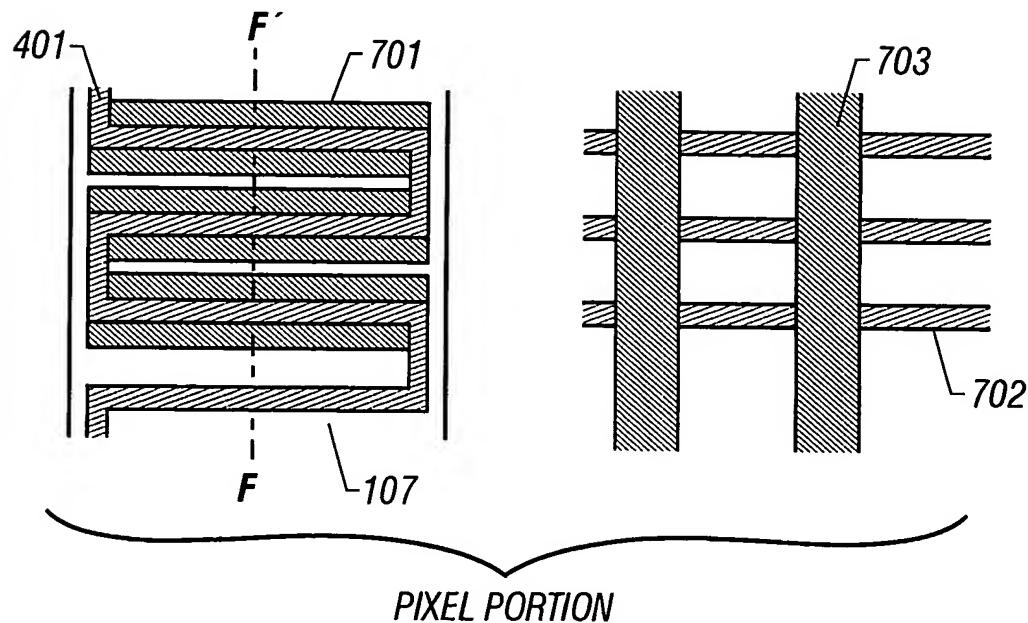


FIG. 14

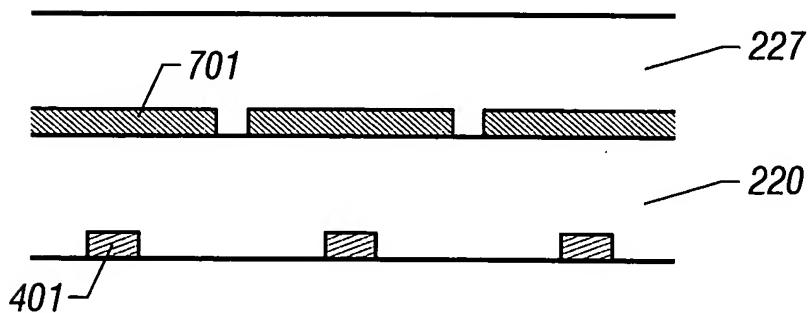


FIG. 15

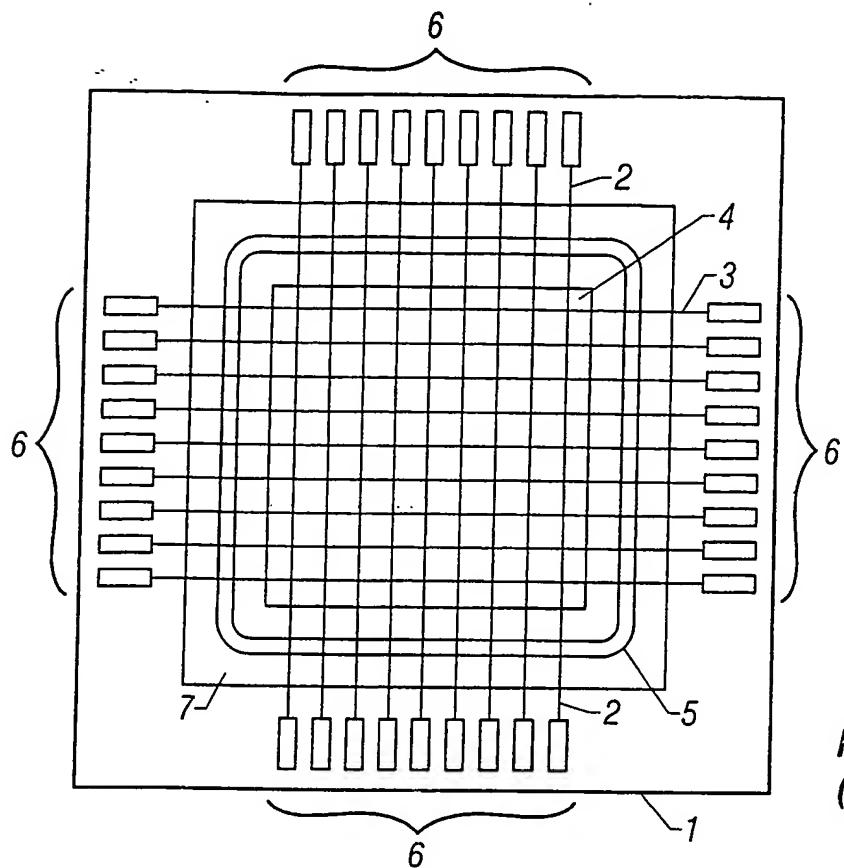


FIG. 16
(PRIOR ART)

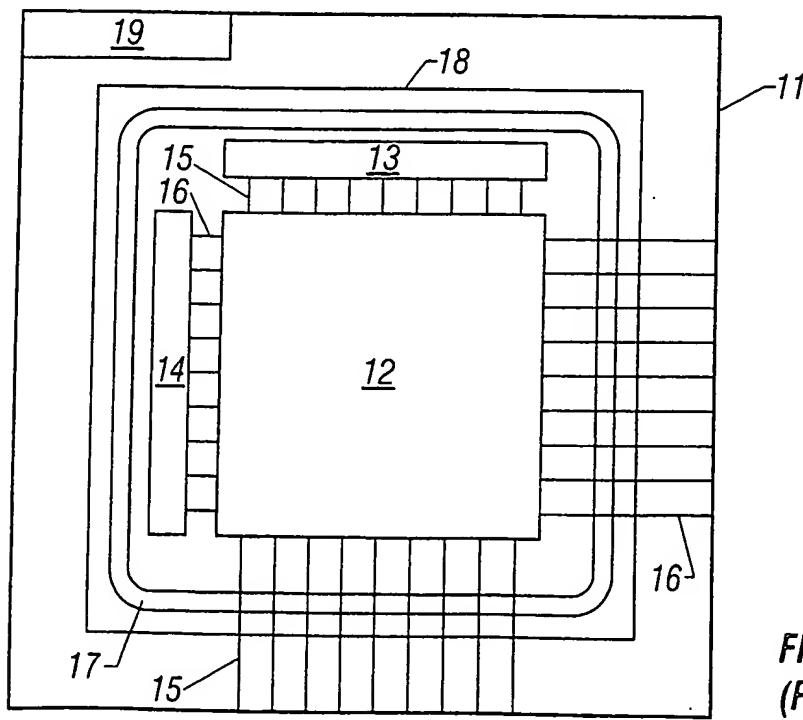


FIG. 17
(PRIOR ART)